

FIG. 3A

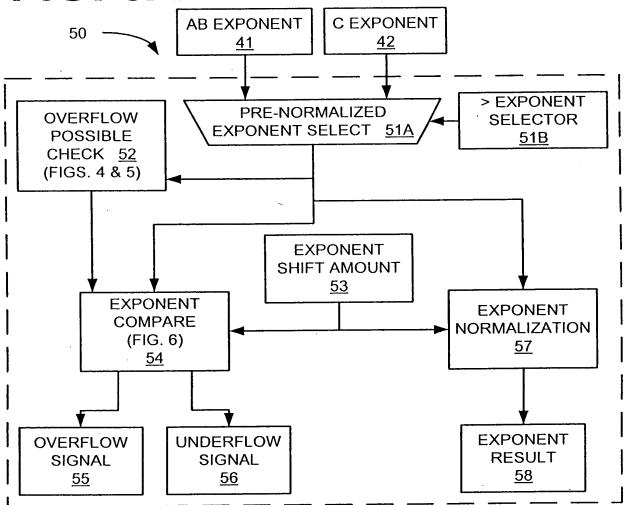


FIG. 3B

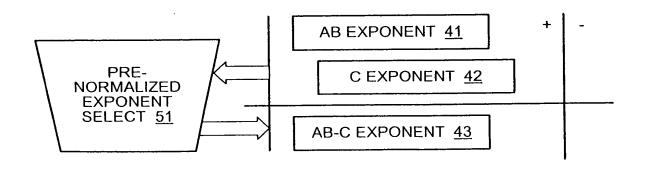
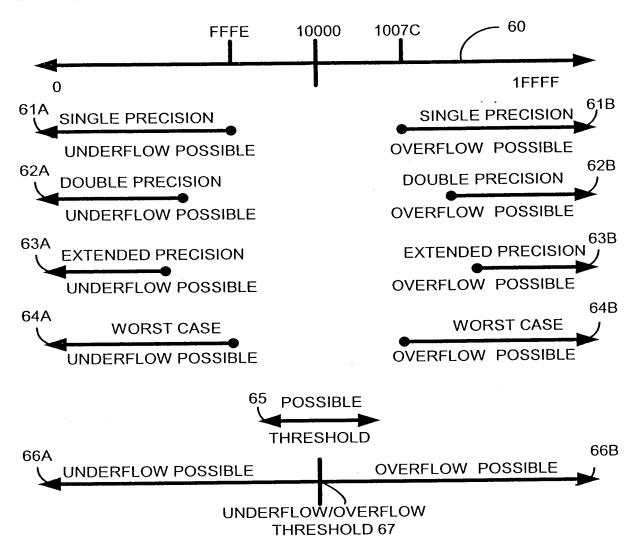


FIG. 3C



F	G	3	D

7 16	15		LSB	OVERFLOW
	15		4 0 1	DOCCIDI E
		•••	10	POSSIBLE
B 68C	68D			1=YES
0	Х	•••	XX	0
1	X	•••	X X	1
0	X	•••	XX	. 1
1	X		X X	1
0	X		X X	0
1	X	•••	X X	0
0	X		X X	0
1	X	•••	X X	0
	0 1 0 1 0	1 X 0 X 1 X 0 X 1 X	0 X 1 X 0 X 1 X 1 X 0 X 1 X 1 X	0 X X X

- 68

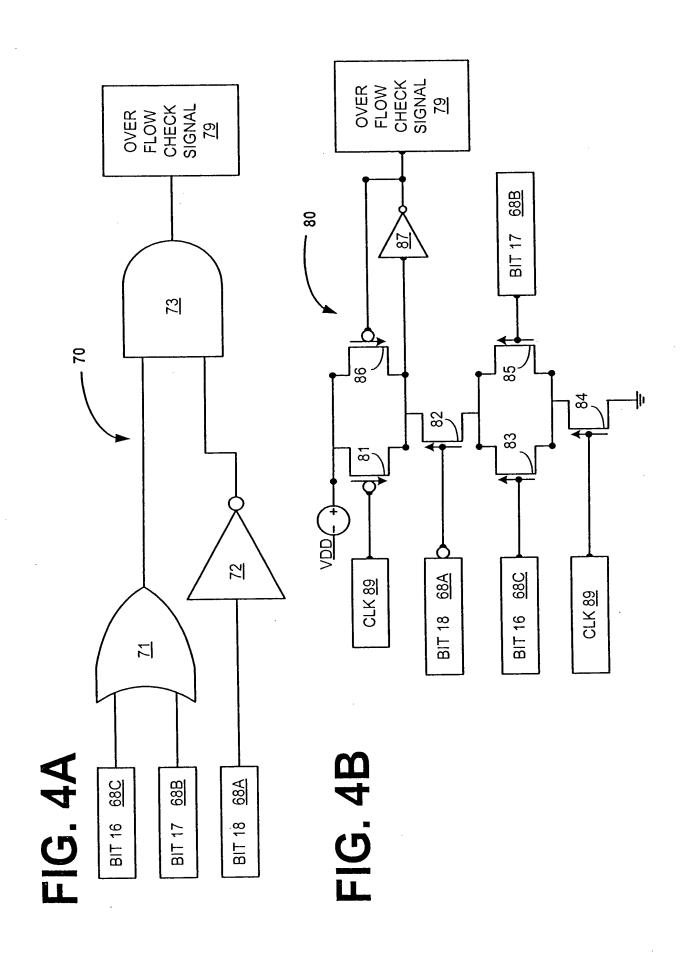
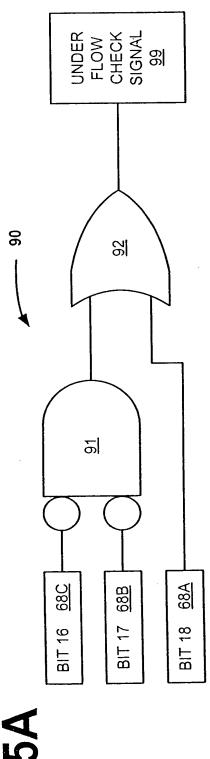
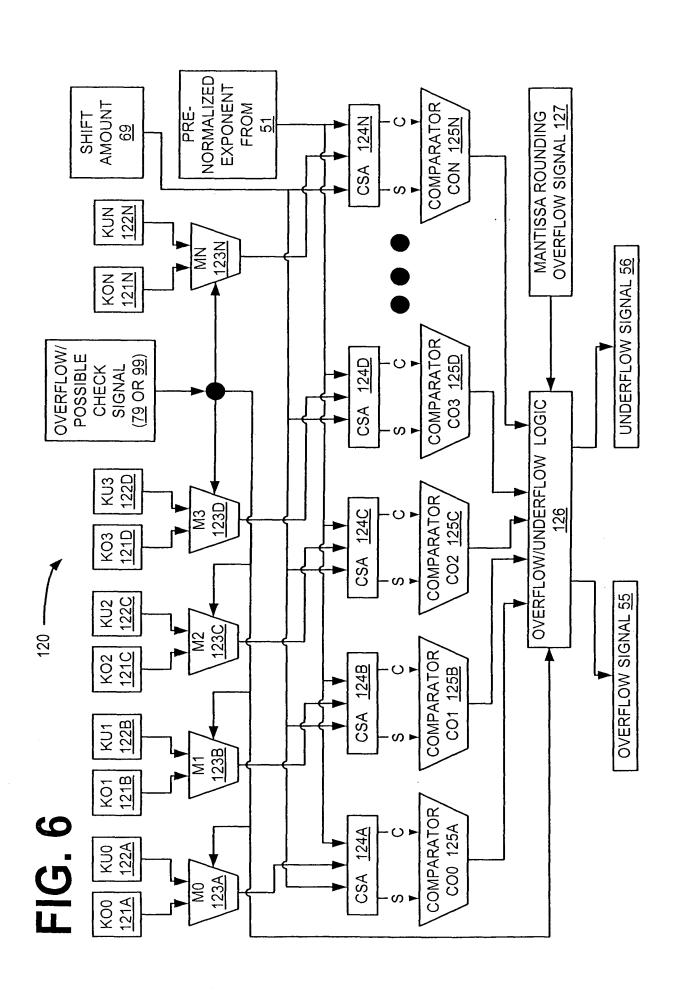


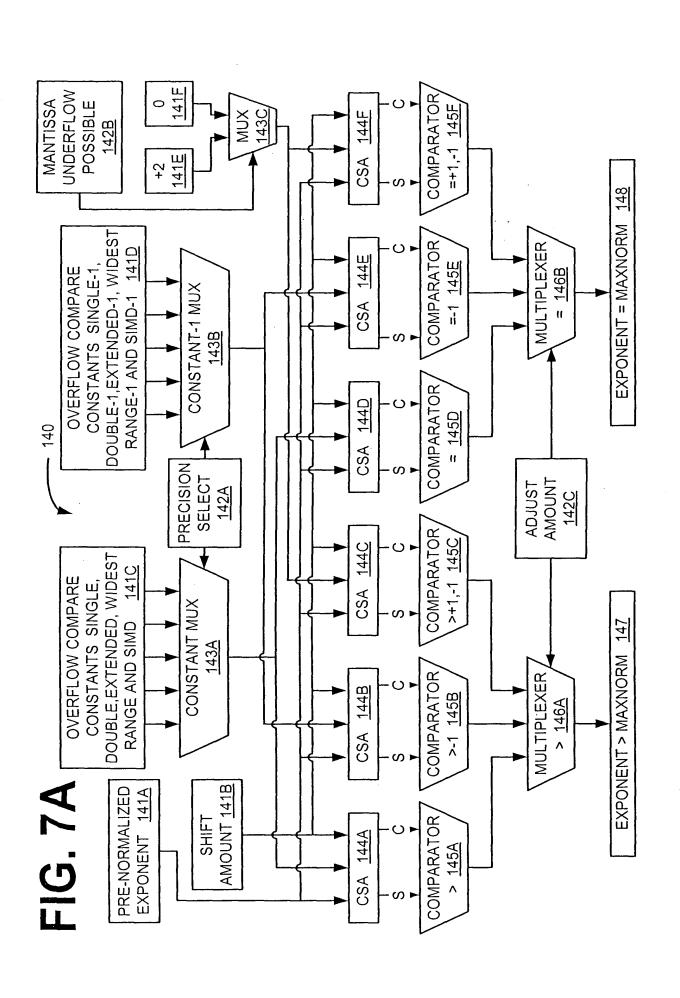
FIG. 5A

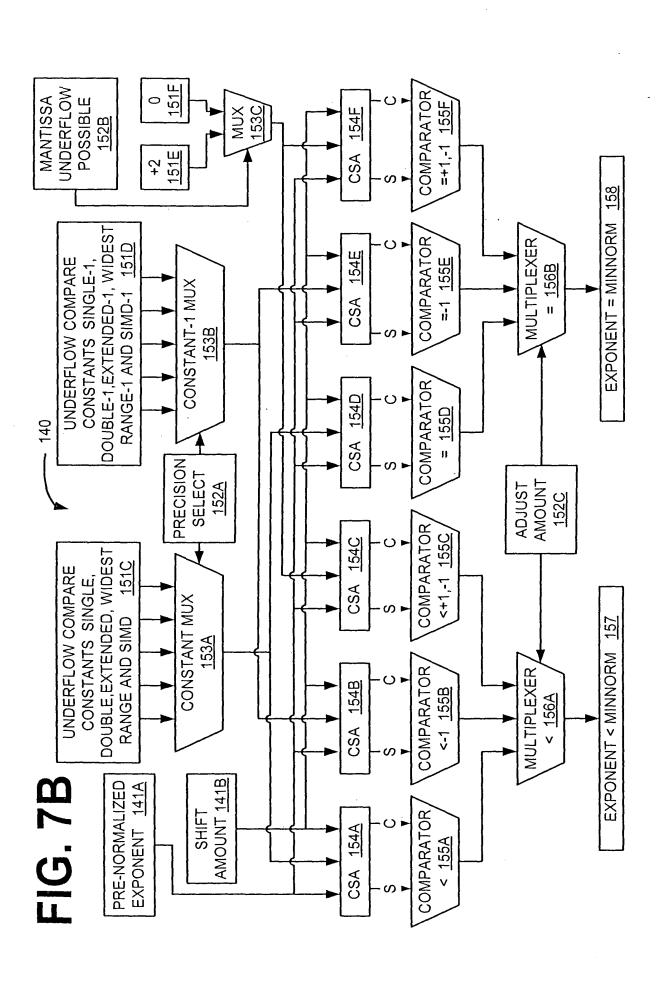


UNDER FLOW CHECK SIGNAL 100 107 68A **BIT 18** 111 104 101 68B 68C CLK 109 CLK 109 **BIT 16 BIT 17**

FIG. 5B







160 **FIG. 8**

